


3.1 IMX6ULL

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3.1.1

IMX6ULL ROM	boot ROM USB	boot ROM Flash	BOOT_MODE[1:0] SD EMMC Flash	eFUSE GPIO
① boot ROM	USB	SD	BOOT_MODE[1:0]	2 BOOT_MODE1 BOOT_
BOOT_MODE[1:0]	4			
<div>image not found or type unknown</div> 				
00 ROM	BT_FUSE_SEL 0	GPIO USB	Boot ROM eFUSE BT_FUSE_SEL 1 eFUSE	eFUSE
01	boot ROM	USB	EMMC	
10		SD EMMC	2	
②				
00	eFUSE			
10	eFUSE	GPIO	eFUSE BT_FUSE_SEL	0 eFUSE
	GPIO	3		
③	eFUSE GPIO			
eFUSE GPIO				
Nand Flash	2048	4096	boot ROM	Nand Flash

eFUSE GPIO

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eFUSE GPIO GPIO eFUSE GPIO eFUSE IMX6ULL Chapter

EMMC EMMC eSDHC1 eSDHC2

TF TF eSDHC1 eSDHC2

EMMC TF eFUSE GPIO IMX6ULL Chapter 5: Fusemap

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BOOT_MODE 0b00 eFUSE eFUSE

BOOT_MODE 0b10 eFUSE GPIO eFUSE GPIO eFUSE BT_FUSE_SEL (

BOOT_MODE 0b10

SD TF 2

a. eFUSE BOOT_CFG1[7:5] 0b010

b. 3.1.3 GPIO boot overrides BOOT_CFG1[7:5] GPIO LCD1_DATA07~05 3 0b010

SD TF eFUSE GPIO

a. eFUSE BOOT_CFG1[4:0]

b. 3.1.3 GPIO boot overrides BOOT_CFG1[4:0] GPIO LCD1_DATA04~00

IMX6ULL SD TF

a. eFUSE BOOT_CFG2[4:3] eSDHC1 eSDHC2

b. 3.1.3 GPIO boot overrides BOOT_CFG2[4:3] GPIO LCD1_DATA12~11

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3.1.2 100ASK_IMX6ULL

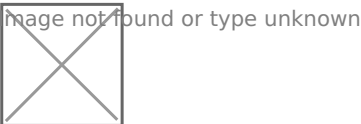
100ASK_IMX6ULL 3 EMMC SD USB

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BOOT CFG				
BOOT	SW1(LCD_DATA5)	SW2(LCD_DATA11)	SW3(BOOT_MODE0)	SW4(BOOT_MODE1)
EMMC	OFF	OFF	ON	OFF
SD	ON	ON	ON	OFF
USB	X	X	OFF	ON

SW3 SW4 BOOT_MODE ON 0 OFF 1 SW3 SW4 ON OFF BOOT_MODE 0b10
BT_FUSE_SEL 0 GPIO LCD1_DATA07~05
100ASK_IMX6ULL SD/TF EMMC LCD1_DATA07~05 0b010 SD/TF LCD1_DATA07~05 01
ATA07~06 0b01 SW1(LCD1_DATA05)
IMX6ULL 2 EMMC
Flash 2 SD/TF LCD1_DATA12~11 0b00 eSDHC1 0b01 eSDHC2 LCD1_DATA1
HC1 100ASK_IMX6ULL TF eSDHC1 OFF 1 eSDHC2 100ASK_IMX6ULL EMMC eSDI

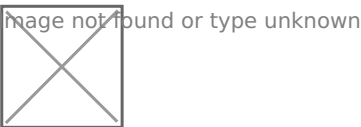
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USB SD TF EMMC EMMC

3.1.3 GPIO boot override

IMX6ULL eFUSE GPIO eFUSE GPIO IMX6ULL Chapter 8:



Revision #3

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