15. LCD

15.1 LCD

15.2.1 LCD

image-202201-121-44941418

LCD

1

DCLK DCLK

2

RGB RGB

3 LCD

HSYNC HSYNC

4

VSYNC VSYNC

5 RGB

RGB DE DE RGB

LCD LCD

image-20220112145025755

DCLK RGB HSYNC VSYNC RGB DE LCD

6 RGB

image-20220112145046895

RGB Framebuffer Framebuffer LC

LCD Framebuffer DCLK VSYNC HSYNC DE

15.2.2 RGB LCD

RGB RGB

1 (DCLK)

LCD DE VS HS RGB

2 RGB R[0:7],G[0:7],B[0:7]

R() G() B 8 24

3 RGB DE

RGB LCD DE HV HV HS VS RGB DE DE RGB LCD

4

HS HSYNC

5

VS VSYNC

6 LCD

GPIO LCD

100ASK IMX6ULL LCD

image-20220112145210913

Image not found or type unknown

15.2.3 TFT

(7 1024600TN-RGB)

TFT 7 1024600TN-RGB

image-20220112145308136

Image not found or type unknown

15.2.4 LCD

1

100ASK_7.0 LCD

image-20220112145317135

Image not found or type unknown

CLK LCD Dn0-Dn7

Hsync LCD HSD HSD thp thb thd thf th

image-20220112145335485

Image not found or type unknown

VSD LCD VSD VSD tvpw tvb tv

image-20220112145341913

Image not found or type unknown

RGB DEN

image-20220112145347564

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51.2Mhz

2RGB

LCD R0-R7 G0-G7 B0-B7 3*8=24 LCD BPP

ARGB888 ARGB555

ARGB888 32 A RGB 8+8+8=24BPP

ARGB555 16 A RGB 5+5+5=15BPP

15.2 IMX6ULL LCD

15.2.1 LCD

IMX6ULL LCD elcdif(LCD)

a. MPU

b. DOTCLK RGB

c.VSYNC

d. 8/16/18/24/32 bit bpp IO

e.MPU VSYNC DOTCLK

image-20220112145424746

Image not found or type unknown

IMX6ULL LCD AXI RGB FIFO FIFO LCD LCD

IMX6ull Chapter 34

Enhanced LCD Interface (eLCDIF)

15.2.2 LCD

image-20220112145449271

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LCD

15.2.2.1 @LCDIF_CTRL

image-20220112145500174

Image not found or type unknown

SFTRST

BYPASS_COUNT DOTCLK DVI modes

DOTCLK_MODE 1 DOTCLK LCD_DATABUS_WIDTH RGB WORD LENGTH **RBG** MASTER LCD DATA_FORMAT_16_BIT 16BPP 15.2.2.2 @LCDIF_CTRL1 image-20220112145520234 Image not found or type unknown BYTE_PACKING_FORMAT 4 RGB A() 15.2.2.3 ③LCDIF_TRANSFER_COUNT image-20220112145543927 Image not found or type unknown **V_COUNT** Χ **H_COUNT** У 15.2.2.4 @LCDIF_VDCTRL0 image-20220112145551594 Image not found or type unknown VSYNC_PULSE_WIDTH VSYNC_PULSE_WIDTH_UNIT VSYNC_PERIOD_UNIT ENABLE_POL DE DOTCLK_POL HSYNC_POL VSYNC POL

VSYNC OEB VSYNC

15.2.2.5 **⑤LCDIF_VDCTRL1**

image-20220112145558654

Image not found or type unknown

VSYNC_PERIOD

15.2.2.6 @LCDIF_VDCTRL2

image-20220112145602359

Image not found or type unknown

HSYNC_PULSE_WIDTH

HSYNC PERIOD

15.2.2.7 ⑦LCDIF_VDCTRL3

image-20220112145625980

Image not found or type unknown

HRIZONTAL_WAIT_CNT

VERTICAL_WAIT_CNT

15.2.2.8 @LCDIF_VDCTRL4

image-20220112145632883

Image not found or type unknown

SYNC SIGNALS ON DOTCLK 1

DOTCLK_H_VALID_DATA_CNT DOTCLK

15.2.2.9 9LCDIF_CUR_BUF

image-20220112145636601

Image not found or type unknown

15.2.2.10 @LCDIF_NEXT_BUF

image-20220112145651586

Image not found or type unknown

ADDR LCD

15.3

003 LCD _ _ P

15.3.3

LCD LCD

15.3.4

① LCD lcd

② CPU LCD cpu

u n

LCD LCD LCD LCD CPU LCD LCD LCD LCD

LCD " "

image-20220112145758887

Image not found or type unknown

 LCD
 imx6ull_con.c
 LCD
 xxx_con

 LCD
 lcd_7_0.c
 LCD
 lcd_xxx.c

Git NoosProgramProject/(15_LCD /01_simple_test/lcd_managerth Git

NoosProgramProject/(15_LCD /01_simple_test/lcd_controller_manager.h)**

004 _L(

15.4.1 LCD

lcd_manager.h LCD bpp lcd_manager.h

```
enum {
\squareNORMAL = 0,
\squareINVERT = 1,
□};
\square/* NORMAL :
□* INVERT :
□*/
_typedef struct pins_polarity {

∏int vclk; /* normal:
pins_polarity, *p_pins_polarity;
_typedef struct time_sequence {
□/* */

__int tvf; /* , Vertical Front porch */
□/* */
mint thb; /* , Horizontal Back porch */
Itime_sequence, *p_time_sequence;
```

lcd_params lcd register_lcd LCD lcd_params select_lcd lcd_params

15.4.2 LCD

lcd_controller_manager.hLCDlcd_controller_ma

```
typedef struct lcd_controller{
    [char* name;
    [void (*init)(p_lcd_params plcdparams);
    [void(*enable)(void);
    [void(*disable)(void);
}lcd_controller, *p_lcd_controller;
```

lcd_controller lcd

lcd_controller_manager.cLCDregister_lcd_controllerLCDlcd_cllerLCDLCDlcd_controllerLCD

15.5 LCD

** Git NoosProgramProject/(15_LCD /01_simple_test/imx6ull_con.c)**

005

0

15.5.1 LCD

image-20220112145945035

Image not found or type unknown

15.1.3 30

15.5.2 LCD

IMX6ULL Chapter 18 Clock Controller Module (CCM)

51.2Mhz

15.5.2.1 ① PLL

image-20220112150005692

Image not found or type unknown

LCD PLL5(video pll)

image-20220112150039275

Image not found or type unknown

VIDEO pll

Video PLL output frequency(PLL5) = Fref * (DIV SELECT +

15.5.2.2 ② PLL

image-20220112150053310

Image not found or type unknown

PLL5 PLL5_MAIN_CLK = PLL5 / POST_DIV_SELECT / VIDEO_DIV

15.5.2.3 ③PLL LCDIF

Image not found or type unknown

PLL5 LCDIF LCDIF1_CLK_ROOT = PLL5_MAIN_CLK /LCDIF1_PRED / LCDIF1_PODF = 32 NUM = 0 DENOM = 0 POST_DIV_SELECT = 1 VIDEO_DIV = 1LCDIF1_PRED = 3 LCDIF1_PODF :

 $24*(32+0)/1/1/3/5 \approx 51.2 \text{Mhz}$:

15.5.3 LCD

15.5.3.1 ①

```
CCM_ANALOG->PLL_VIDEO_NUM = 0; []
CCM_ANALOG->PLL_VIDEO_DENOM = 0;
```

15.5.3.2 ② CCM_ANALOG_PLL_VIDEOn

image-20220112150211473

Image not found or type unknown

```
CCM_ANALOG->PLL_VIDEO = (2 << 19) | (1 << 13) | (32<< 0);
```

PLL5 32 1 24M PLL5 24 * 32 / 1

15.5.3.3 3 CCM_ANALOG_MISC2n

image-20220112150247815

Image not found or type unknown

1 PLL5 24 * 32 / 1 / 1 = 768 Mhz

15.5.3.4 @ CCM CSCDR2

image-20220112150300024

Image not found or type unknown

3768/3 = 256Mhz

```
[CCM->CSCDR2 &= ~(7 << 15); []

[CCM->CSCDR2 |= (2 << 15);

[CCM->CSCDR2 &= ~(7 << 12); []

[CCM->CSCDR2 |= (2 << 12); []

[CCM->CSCDR2 &= ~(7 << 9);
```

15.5.3.5 ⑤ CCM_CBCMR

image-20220112150322222

Image not found or type unknown

```
[CCM->CBCMR &= ~(7 << 23); []]]]

[CCM->CBCMR | =[4 << 23; []/*[25:23] : 4 : 5 */
```

5 51.2Mhz

15.5.3.6 @

image-20220112150341565

Image not found or type unknown

```
/* LCD LCD */

| LCDIF->CTRL = 1<<31;

/* */
| delay(100);

/* */
|LCDIF->CTRL = 0<<31; /* */
```

15.5.4 LCD

15.5.4.1 ① LCDIF_CTRLn

image-20220112150402916

Image not found or type unknown

15.5.4.2 ② LCDIF_CTRL1n

image-20220112150501256

Image not found or type unknown

ARGB 24 A 16bpp ARGB555

15.5.5 LCD

15.5.5.1 ① LCDIF_TRANSFER_COUNT

image-20220112150610501

Image not found or type unknown

```
```c
```

LCDIF->TRANSFER\_COUNT = (plcdparams->yres << 16) | (plcdparams->xres << 0); ```

#### 15.5.5.2 ② LCDIF\_VDCTRL0n

image-20220112150638122

Image not found or type unknown

#### 15.5.5.3 ③ LCDIF\_VDCTRL1

image-20220112150655412

Image not found or type unknown

```
LCDIF->VDCTRL1 = plcdparams->time_seq.tvb + plcdparams->time_seq.tvp + plcdparams->yres +
plcdparams->time_seq.tvf;
```

: tvb+ tvp+ yres+ tvf

#### 15.5.5.4 ④ LCDIF VDCTRL2

image-20220112150744101

Image not found or type unknown

```
LCDIF->VDCTRL2 = (plcdparams->time_seq. thp << 18) | (plcdparams->time_seq. thb + plcdparams-
>time_seq. thp + plcdparams->xres + plcdparams->time_seq. thf);
```

[18:31]:

[17:0]:

: thb+ thp+ xres+ thf

#### 15.5.5.5 ⑤ LCDIF\_VDCTRL3

image-20220112150809660

Image not found or type unknown

```
LCDIF->VDCTRL3 = ((plcdparams->time_seq.thb + plcdparams->time_seq.thp) << 16) | (plcdparams->time_seq.tvb + plcdparams->time_seq.tvp);
```

**ELCDIF VDCTRL3** 

[27:16] = thb + thp

[15:0]: = tvb + tvp

## 15.5.5.6 @ LCDIF\_VDCTRL4

image-20220112150833971

Image not found or type unknown

```
LCDIF->VDCTRL4 = (1<<18) | (plcdparams->xres);
```

**ELCDIF VDCTRL4** 

[18]: VSHYNC HSYNC DOTCLK 1

17:0:

## 15.5.6

image-20220112150949262

Image not found or type unknown

```
LCDIF->CUR_BUF = plcdparams->fb_base;
LCDIF->NEXT_BUF = plcdparams->fb_base;
```

CUR\_BUF:

NEXT\_BUF:

# 15.5.7 \_LCD

\*\* Git NoosProgramProject/(15\_LCD /01\_simple\_test/lcd\_7\_0.c)\*\*

006 \_LC

# 15.5.8 LCD

```
.name = "lcd_7.0",
```

LCD LCD

# 15.5.9

```
LCDIF_VDCTRL0n
```

image-20220112151048189

Image not found or type unknown

```
VSYNC_POL HSYNC_POL 0 1
```

DOTCLK POL 0 1

ENABLE\_POL 0 1

```
15 1.4 DCLK HSD VSD DEN
```

# 15.5.10

image-20220112151141164

Image not found or type unknown

```
lcd_7_0.c lcd_7_0_params
```

1 Thpw tvb = 20

```
2 Thb thb = 140

3 Thfp thf = 160

4 THA tvp = 1024

5 Tvpw tvp = 3

6 Tvb thp = 20

7 Tvfp tvf = 12

8 Tvd yres = 600
```

# 15.5.11

image-20220112151220548

Image not found or type unknown

```
imx_6ull 2GB 100ask_imx_6ull 512M 0x80000000~0xaC xa0000000 6004=2462600≈2.4MB
```

0x81000000~0xa0000000 2.4MB

# 15.6

**LCD** 

\*\* Git NoosProgramProject/(15\_LCD /01\_simple\_test)\*\*

007 \_LCD \_

# 15.6.1 LCD

```
[]/* LCD */[]
[]lcd_7_0_add();
[]/* LCD */[]
[]lcd_contoller_add();
```

lcd\_manager.c lcd\_controller\_manager.c LCD LCD

```
[/* Imx6ull LCD */[]

[select_lcd_controller("Imx6ull"); []

[/* LCD */[]

[select_lcd("lcd_7.0");
```

lcd manager.c lcd controller manager.c LCD LCD

```
lcd_controller_init(g_p_lcd_selected);
```

lcd\_controller\_init LCD LCD

# 15.6.2 LCD

g\_p\_lcd\_controller\_selected LCD

g\_p\_lcd\_controller\_selected LCD LCD lcd\_controller\_enable

```
static void Imx6ull_lcd_controller_enable(void)
{

CLCDIF->CTRL | = 1<<0; /* 6ULL LCD */
}</pre>
```

imx6ull LCD g\_p\_lcd\_controller\_selected->enable() Imx6ull\_lcd\_controller\_ena

### 15.6.3 LCD

lcd

fb\_base xres yres bpp

## 15.6.4 framebuffer

16bpp 32bpp framebuffer

```
p = (unsigned short *) fb_base;

for (x = 0; x < xres; x++)

for (y = 0; y < yres; y++)

**p++ = 0x7c00;

**p = (unsigned short *) fb_base;

for (x = 0; x < xres; x++)

for (y = 0; y < yres; y++)

for (y = 0; y < yres; y++)

**p++ = 0x3E0;

**precise of the property of the proper
```

lcd bpp 16

bpp 16 lcd ARGB555 ARRRRRGGGGGBBBBB 5

lcd bpp 32 bpp 32 lcd ARGB888 AAAAAAAARRRRRRRRGGGGGGGGBE

# 15.7

\*\* Git NoosProgramProject/(15\_LCD /02\_dot\_line\_circle)\*\* 008 LCD \_

# 15.7.1

① (x y) FB

Image not found or type unknown

```
(x y) = fb_ba_s e + \frac{1}{2} \frac{b_p s}{b_p s} = \frac{1}{2} \frac{b_p s}{b_p s}
```

② frambuffer.c LCD

③ LCD bpp 32 bpp 16 32 16 16bpp

```
int r = (rgb >> 16) & 0xff;
[int g = (rgb >> 8) & 0xff;
[int b = rgb & 0xff;

[/* argb555 */
[r = r >> 3;
[g = g >> 3;
[b = b >> 3;
[return ((r<<10) | (g<<5) | (b));</pre>
```

④ x y 16PP 16 (2 ) unsigned short 32PP

```
mipdw = (unsigned int *) pixel_base;
mipredw = color;
mipreak;
mipreak
```

# 15.7.2

"C LCD "

http://blog.csdn.net/p1126500468/article/det

geometry.c

```
fb_get_lcd_params();

[delay(100000);

[delay(100000);

[delay(100000);

[draw_line(xres - 1, 0, xres - 1, yres - 1, 0xffff00);

[delay(100000);

[draw_line(0, yres - 1, xres - 1, yres - 1, 0xff00aa);

[delay(100000);

[draw_line(0, 0, 0, yres - 1, 0xff00ef);

[delay(100000);

[draw_line(0, 0, xres - 1, yres - 1, 0xff4500);

[delay(100000);

[draw_line(xres - 1, 0, 0, yres - 1, 0xff0780);
```

# 15.7.3

"C LCD "

http://blog.csdn.net/p1126500468/article/det

geometry.c

image-20220112151929285

Image not found or type unknown

```
draw_circle(xres/2, yres/2, yres/4, 0xff00);
```

# 15.8

```
** Git NoosProgramProject/(15_LCD /03_font_test)** 009 _LCD _ _
```

## 15.8.1 LCD

```
font.c LCD LCD LCD
```

## 15.8.2

Linux "font" font\_8x16.c A

```
□/* 65 0x41 'A' */
/ Dx00, / 00000000 */
□9x00, /* 00000000 */
□0x10, /* 00010000 */
□9x38, /* 00111000 */
□9x6c, /* 01101100 */
□9xc6, /* 11000110 */
□0xc6, /* 11000110 */
□0xfe, /* 111111110 */
□9xc6, /* 11000110 */
□9xc6, /* 11000110 */
□9xc6, /* 11000110 */
□0xc6, /* 11000110 */
□0x00, /* 00000000 */
□0x00, /* 00000000 */
□9x00, /* 00000000 */
```

```
ascii fontdata_8x16
a.
b.
С.
 void fb_print_char(int x, int y, char c, unsigned int color)
 \square{
 \Boxint i, j;
 \squareconst unsigned char *dots = &fontdata_8x16[c * 16];
 □unsigned char data;
 □/*
 \square{

 data = *dots++;

 \Box \Boxbit = 7;
 \squarefor (i = x; i < x+8; i++)
 III/*
 */
 ____if (data & (1<<bit))
 fb_put_pixel(i, j, color);
 III}
 B
 16
font_8x16.c
 16
 ascii
 fb_put_pixel()
 1
 16
```

"A"

LCD

□0x00, /\* 00000000 \*/

8\*16

0

1

x 8 xres y+16

```
[void fb_print_string(int x, int y, char* str, unsigned int color)
\mathbb{R}
\coprodint i = 0, j;
\square{
 \iiint y = y+16;
melse if (str[i] == '\r')
\square \square x = 0;
∏else
\square I
fb_print_char(x, y, str[i], color);
mif(x >= xres) /* */
\Box\Box{
\iiint y = y+16;
\square
\square
∭i++;
|||
```

```
fb_print_string(10, 10, "www.100ask.net\n\r100ask.taobao.com", 0xff00);
```

## 15.8.4 4-1.4

Git NoosProgramProject/(15\_LCD /03\_font\_test)

# 15.8.5 3-1.4

RGB

Updated 3 March 2022 02:47:06 by